FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. MNFRAME.033A	APPLICATION NO. 08/943,356 RECEIVED
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)		APPLICANT Chari et al.	JUL 0 7 2000
		FILING DATE October 1, 1997	GROUP 2758 GROUP 2700

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
S.V.	1	5,274,680	12/28/93	Sorton et al.	375	118	
S.V.	2	5,666,362	09/09/97	Chen et al.	370	420	
	3	5,799,016	08/25/98	Onweller	370	401	01/11/96
	4	5,892,912	04/06/99	Suzuki et al.	395	200.48	10/29/96
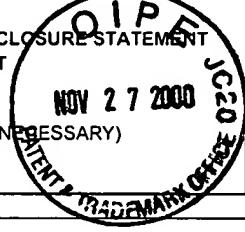
FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO

EXAMINER INITIAL		OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
S.V.	5	Sanchez et al., *Abstract* **CAMAD '94**, IEEE Workshop on Computer Aided Design and Modeling, page 34, 1994,
		"DISNES: A Distribured Network Emulation System for Gigabit Networks."
S.V.	6	Truong et al., *IEEE Communications Magazine*, pp. 70-85, may 1995,
		"LAN Emulation on an ATM Network."
		Not in case

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EXAMINER		DATE CONSIDERED 1/2/00

*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. MNFRAME.033A	APPLICATION NO. 08/943,356	#24
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICANT Chari, et al.		RECEIVED
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NOV 2 7 2000

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
S.J	1	5,919,258	07/06/99	Kayashima et al.	713	201	02/06/97

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

EXAMINER INITIAL		OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
S.J.	2	Chao et al., *Global Telecommunications Conference*, **Globecom '91**, Vol. 2, pp. 968-975, 1991, "Transport of Gigabit/sec Data Packets over SONET/ATM Networks."
S.J.	3	Hsiao, S. and C.Y. Chen, *Parallel Processing Symposium*, pp. 773-777, IEEE 1993, "A New Model for the Performance Evaluation of Synchronous Circuit Switched Multistage Interconnection Networks." No date
S.J.	4	Tsun-yuk Hsu, W. and P.C. Yew, *Parallel Processing Symposium*, pp. 309-317, IEEE 1991, "An Effective Synchronization Network for Large Multiprocessor Systems."
S.J.	5	Yun, K. and D. Dill, *Computer Aided Design*, pp. 255-260, IEEE 1993, "Unifying Synchronous/Asynchronous State Machine Synthesis."

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EXAMINER		DATE CONSIDERED 1/2/0

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